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the storing section and display section being provided in each dot which is a minimum unit of display and being arranged on the at least one of a semiconductor and an insulating substrate.

2. (Amended) A display device, comprising:

at least one of a semiconductor and an insulating substrate;

a storing section configured by at least one storing circuit provided at each of interconnections of a plurality of write lines and a plurality of data lines correspondingly to an array pattern of a dot as a minimum unit of display so that, when a write signal is transmitted through said write line and an image signal as a digital data signal to control display is transmitted through said data line, the image signal is stored;

a converting section that converts a value based on a value of the image signal stored by said storing section into an analog signal; and

a display control section that performs tonal control using a liquid crystal on the basis of an analog signal converted by said converting section;

the storage section and display section being provided in each dot which is a minimum unit of display and being arranged on the at least one of a semiconductor and an insulating substrate.

3. (Amended) The display device as claimed in claim 2, said storing circuit of said storing section being configured by a static circuit.

4. (Twice Amended) The display device as claimed in claim 2, said converting section pulse-width-modulating the value based on the image signal to convert the value into the analog signal.

5. (Twice Amended) The display device as claimed in claim 2, said converting section converting the value based on the image signal into the analog signal modulated to a pulse width based on a γ -characteristic.

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6. (Twice Amended) The display device as claimed in claim 2, said converting section performing conversion into the analog signal at a constant period interval.

7. (Amended) The display device as claimed in claim 6, a duration that no conversion into the analog signal is made being provided in the constant period.

8. (Amended) The display device as claimed in claim 7, each converting section being different in a start time of the constant period, and the period and the duration no conversion into the analog signal is made being different.

9. (Amended) The display device as claimed in claim 6, an alternating current drive voltage corresponding to the constant period being applied to said display control section.

10. (Amended) The display device as claimed in claim 9, the alternating current drive voltage being a voltage driven at $V_{COM} \pm V_a$ with respect to a reference voltage V_{COM} .

11. (Amended) The display device as claimed in claim 9, the alternating current drive voltage being a voltage alternating-current-inversion-driven by two voltage-applying lines laid correspondingly to said dot array pattern.

12. (Amended) The display device as claimed in claim 9, a plurality of rows of said dot array being provided by groups, and rows in pair being set in each of the groups to invert a phase of the alternating current drive voltage applied.

13. (Twice Amended) The display device as claimed in claim 2, said display control section controlling light emission of current-driven luminescent devices in connection on the basis of the analog signal in place of performing tonal control using a liquid crystal, thereby effecting tonal control.

14. (Amended) A display device, comprising:
at least one of a semiconductor and an insulating substrate;
a storing section configured by at least one storing circuit provided at each of interconnections of a plurality of write lines and a plurality of data lines correspondingly to an array pattern of a dot as a minimum unit of display so that, when a write signal is transmitted through

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at least one active device provided respectively connected to said storing circuits of said storing section and current-driven luminescent devices having areas corresponding to place values represented by image signals stored in said storing circuits; and

a display control section provided in each dot to control emission of light of said current-driven luminescent devices on the basis of the values of the image signals stored in said storing circuits, the storing section and the display control section being arranged on the at least one of a semiconductor and an insulating substrate.

15. (Twice Amended) The display device as claimed in claim 13, said current-driven luminescent devices being structured by EL devices.

16. (Twice Amended) The display device as claimed in claim 13, said current-driven luminescent devices being structured by organic EL devices.

17. (Amended) A display device, comprising:

at least one of a semiconductor and an insulating substrate;

a storing section configured by at least one storing circuit provided at each of interconnections of a plurality of write lines and a plurality of data lines correspondingly to an array pattern of a dot as a minimum unit of display so that, when a write signal is transmitted through said write line and an image signal as a digital data signal to control display is transmitted through said data line, the image signal is stored;

at least one active device provided respectively connected to said storing circuits of said storing section and liquid crystal driving sections having areas corresponding to place values represented by image signals stored in said storing circuits; and

a display control section provided in each dot to perform tonal control using a liquid crystal on the basis of the values of the image signals stored in said storing circuits, and the storing

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section and the display control section being arranged on the at least one of a semiconductor and an insulating substrate.

18. (Twice Amended) The display device as claimed in claim 2, further including a plurality of read lines laid correspondingly to said dot array pattern so that, if a read signal is transmitted, the image signals stored in said storing circuits are read out of said storing section.

19. (Amended) A display device, comprising:

at least one of a semiconductor and an insulating substrate;

a display drive section having a plurality of word lines, a plurality of write lines and a plurality of data lines laid correspondingly to an array pattern of a dot as a minimum unit of display, and a display control section operating, when at least a write signal is transmitted through said write lines and the image signals are transmitted through said data lines, on the basis of said storing section that stores the image signals, the image signals and a word signal transmitted through said word lines, provided in each of the dot array patterns;

a word line driver section that controls transmission of a word signal to said word lines;

a row decoder section that selects a row for transmitting a write signal to said write lines, to transmit the write signal to a selected row;

a column decoder section that selects said data line;

a column selection switch section that transmits the image signals as data signals to control display to said data line selected by said column decoder section;

the sections being integrated and integrally formed on the at least one of a semiconductor and an insulating substrate.

20. (Amended) The display device as claimed in claim 19, further including a converting section that converts a value based on the image signals stored in said storing section into an analog signal provided in each dot array pattern in said display drive section, and said display control section operates on the basis of the analog signal and the word signal.

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21. (Amended) The display device as claimed in claim 19, said word lines being laid to transmit the word signal to said display control section on two rows.

22. (Twice Amended) The display device as claimed in claim 19, said word line driver section and said row decoder section being allocated correspondingly to a length of said display drive section in a column direction, and said column decoder section and said column selection switch section being allocated correspondingly to a length of said display drive section in a row direction.

23. (Amended) The display device as claimed in claim 19, each column selection switch structuring said column selection switch section being allocated correspondingly to a width of said dot array pattern.

24. (Twice Amended) The display device as claimed in claim 19, said row decoder section selecting a row for transmitting the write signal on the basis of an address signal representing a storage position.

25. (Amended) The display device as claimed in claim 23, said column decoder section selecting said data lines on the basis of the address signal.

26. (Amended) The display device as claimed in claim 25, three dots that develop and display in red, blue and green as light source colors being provided as one pixel, the image signals being input on a one-pixel-unit basis, and said column decoder section selecting data lines to store the image signals in an amount of one pixel.

27. (Amended) The display device as claimed in claim 25, three dots that develop and display in red, blue and green as light source colors being provided as one pixel, the image signals being input on a plural-pixel-unit basis, and said column decoder section selecting data lines to store the image signals in an amount of a plurality of pixels.

28. (Amended) The display device as claimed in claim 19, further including:
a timing controller section that controls at least timing of transmitting the address signal;